

## **SYSTEM AND METHOD FOR MANAGING A CACHE MEMORY**

### **Abstract of the Disclosure**

A processing system optimized for data string manipulations includes data string execution circuitry associated with a bus interface unit or memory controller. Cache coherency is maintained, and data move and compare operations may be performed efficiently on cached data. A barrel shifter for realignment of cached data during move operations and comparators for comparing a test data string to cached data a cache line at a time may be provided.

H:\DOCS\CAF\CAF-2184.DOC  
112603